

THE COMMISSIONER OF PATENTS AND TRADEMARKS, Washington, D.C. 20231

Enclosed for filing is the patent application of Inventor:
NAVEED MAJID, TON MOBERS and SATYEN MUKHERJEEMULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN HIGH-
POWER APPLICATIONS

66/51/99

ENCLOSED ARE:

- Appointment of Associates;
- Information Disclosure Statement, Form PTO-1449 and copies of documents listed therein;
- Preliminary Amendment;
- Specification (10 Pages of Specification, Claims, & Abstract);
- Declaration and Power of Attorney:
(4 Pages of a [X]fully executed []unsigned Declaration);
- Drawing (1 sheet of [X]informal []formal sheets);
- Certified copy of application Serial ;
- Authorization Pursuant to 37 CFR §1.136(a)(3)
- Other: ;
- Assignment to PHILIPS ELECTRONICS NORTH AMERICA CORPORATION.

FEE COMPUTATION

CLAIMS AS FILED				
FOR	NUMBER FILED	NUMBER EXTRA	RATE	BASIC FEE - \$760.00
Total Claims	8 - 20 =		X \$18 =	0.00
Independent Claims	2 - 3 =		X \$78 =	0.00
Multiple Dependent Claims, if any			\$260 =	0.00
TOTAL FILING FEE				= \$760.00

Please charge Deposit Account No. 14-1270 in the amount of the total filing fee indicated above, plus any deficiencies. The Commissioner is also hereby authorized to charge any other fees which may be required, except the issue fee, or credit any overpayment to Account No. 14-1270.

[] Amend the specification by inserting before the first line as a centered heading --Cross Reference to Related Applications--; and insert below that as a new paragraph --This is a continuation-in-part of application Serial No. , filed , which is herein incorporated by reference--.

CERTIFICATE OF EXPRESS MAILINGExpress Mail Mailing Label No. EL 335 549 207 US
Date of Deposit November 15, 1999

I hereby certify that this paper and/or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Patti DeMichele
Typed Name


Signature



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
NAVEED MAJID ET AL

Atty. Docket

PHA 23,843

Serial No.

Filed: CONCURRENTLY

MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN HIGH-
POWER APPLICATIONS

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

AUTHORIZATION PURSUANT TO 37 CFR §1.136(a)(3)
AND TO CHARGE DEPOSIT ACCOUNT

Sir:

The Commissioner is hereby requested and authorized to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time.

Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Respectfully submitted,

By 
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